

REMARKS

Claims 1-32 are pending in this application. Each of claims 1, 13 and 21 has been amended to further define the invention. Claim 22 has been amended to clarify antecedent basis as a result of the amendment to claim 21. No new matter has been entered by way of these amendments.

Applicants respectfully request reconsideration of the rejection of claims 1-32 under 35 U.S.C. § 103 as being unpatentable over U.S. Patent No. 6,369,826 to Shimotono et al. (hereafter Shimotono) in light of the amendments and arguments presented below.

The Examiner asserts that Shimotono discloses all of the features of independent claim 1. Claim 1, as amended, includes the feature that the second set of display data is not stored in the memory, among other features. As agreed to by the Examiner during the telephone interview of March 7, 2005, this feature is not disclosed nor taught by Shimotono. Shimotono requires that both the on screen image data and the off screen image data be stored in VRAM 131 (See, Shimotono, figures 2 and 3 and column 3, lines 53-57.) As discussed in the phone interview, claim 1, as amended, is patentable over Shimotono for at least this reason. Each of claims 2-12 ultimately depends from claim 1 and is likewise patentable over Shimotono for at least the same reasons as claim 1.

Applicants would also like to point out that in the Office action dated January 11, 2005; the Examiner acknowledges that Shimotono fails to disclose a display panel that includes a memory region as specified in claim 5. The Examiner did not specify a basis for maintaining the rejection of claim 5. The Examiner referred to column 1, lines 45-55 of Shimotono as disclosing this feature. Applicants respectfully disagree with this characterization, as this section refers to the computer memory for storing the on screen and off screen image data and not a display panel with a memory region (see figure 1, LCD panel 133, and column 3, lines 11-15 of Shimotono). Applicants respectfully submit that claim 5 is patentable over Shimotono for the additional reason that Shimotono is silent as to a display panel having a memory region. Furthermore, there would be no motivation to modify Shimotono to include a display panel

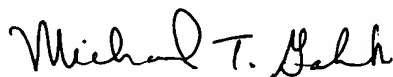
having a memory region, as there is no need for a memory region within the display panel of Shimotono.

Independent claim 13, as amended, includes, among other features, the feature that the second set of display data is not stored in the memory. As discussed above with respect to claim 1, Shimotono fails to disclose or teach this feature. Accordingly, amended claim 13 and dependent claims 14-20 are patentable over Shimotono.

Similarly, independent claim 21, as amended, includes the feature of the second set of display data not being stored within a memory region storing the first set of display data. As discussed above, Shimotono fails to disclose or teach this feature. Accordingly, amended claim 21 and dependent claims 22-32 are patentable over Shimotono.

Applicants appreciate the Examiner's time for the telephone interview of March 7, 2005. In view of the foregoing, Applicants respectfully submit that all of the pending claims are in condition for allowance. A notice of allowance is respectfully requested. In the event a telephone conversation would expedite the prosecution of this application, the Examiner may reach the undersigned at (408) 952-6126.

Respectfully submitted,



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